

MemoCIS 1st Workshop and MC Meeting
'Memristors – Devices, Models, Circuits, Systems and Applications', Lisbon, Portugal.
9.-11.5.2015

PROGRAM

Day 1 - Thursday, May 28, 2015

08:30 – 09:00

Registration and Welcome

09:00 – 10:00

Keynote: Daniele Ielmini “Resistive switching devices: switching, reliability and novel applications”

10:00 – 10:30

Yakov Roisin “Single Poly Memristor-Like Devices in CMOS Process Flows”

10:30 – 11:00

Coffee Break

11:00 – 11:20

Martin Klimo, “Fuzzy logic with memristors”

11:20 – 11:40

Antony Kenyon “Silicon oxide – a dynamic Electronic Material for Resistance Switching”

11:40 – 12:00

Eleni Vasilaki “Synaptic dynamics and STDP in Neural Networks and Memristive devices”

12:00 – 12:20

Adnan Mehonic “Silicon Oxide as an Intrinsic Resistance Switching Material”

12:20 – 12:40

Joao Ventura “Resistive switching in MgO and Ag₂S-based Structures”

12:40 – 13:00

Damien Querlioz “Spin-Transfer Torque Magnetic Memory as a Stochastic Memristive Synapse”

13:00 – 14:00

Lunch Break

14:00 – 14:30

Alex Serb, “The right tool for the right job: developing tools to overcome the RRAM testing bottleneck.”

14:30 – 15:30

Management Committee Meeting (Part 1)

15:30 – 16:00

Coffee Break

16:00 – 17:00

Management Committee Meeting (Part 2)

17:00 – 18:00

Round Table Discussion on “Memristor Device Technologies”

20:00 – 21:00

Dinner



PROGRAM

Day 2 - Friday, May 29, 2015

08:30 – 09:30

Keynote: Themis Prodromakis, “Practical Applications of Solid-state Memristors”

09:30 – 10:30

Invited Speaker: Farnood Merrikh Bayat, “Next Generation of Neuromorphic Hardware”

10:30 – 11:00

Morning Break

11:00 – 11:20

Daniele Ielmini, ‘Logic computing with resistive switching devices’

11:20 – 11:40

Georgios Sirakoulis, “Methodology for design of memristor-based circuits on Nano/CMOS hybrid crossbar architecture.”

11:40 – 12:00

Ondrej Such, “On timing-dependent plasticity”

12:00 – 12:20

Sabina Spiga, “Hafnium oxide based memristive devices”

12:20 – 12:40

Maciej Ogorzalek, “Perspectives for 3D integrated heterogeneous systems including memristor-based blocks”

12:40 – 13:00

Plotog Ioan, “EU Directives and consequences over research activities in electronics”

13:00 – 14:00

Lunch Break

14:00 – 15:00

Individual WG Meeting Discussions

15:00 – 15:30

Round Table Discussion on “Memristor Proposals within H2020 and Elsewhere”

15:30 – 16:00

Coffee Break

16:00 – 18:00

One-on-one Meetings for Collaborations/Research Coordination

